PRODUCT AFFECTED:

JP-6

TO SERVICE ENGINEERS:

SUBJECT

The countermeasure for LFO leakage

This information is for solution to two kinds of LFO leakage problems on JP-6 which occurs in two different panel setting conditions as below:

IFO leakage-A 1.

Panel setting and the phenomenon:

An LFO leakage can be audible as a noise in synchronization at the LFO rate when some controls are set at the following positions (even when control "LFO" in VCA section is set at "O" position):

> "RATE" in LFO-1 section "ENV-2 LEVEL" in VCA section : "10" : "0" "LFO" in VCA section

The cause and Countermeasure:

This LFO leakage is caused among wires which are wired between CPU board and MODULE (2 VOICES)/ MODULE (4 VOICES) boards. Please separate a bundle of the wires into two bundles to eliminate the IFO leakage. Please refer to the attached drawing.

NOTE: The above modification has already been taken to JP-6 from the serial number 280950. Accordingly, this modification is effective on JP-6 with serial numbers from 250100 to 280949.

2. LFO leakage-B

Panel setting and phenomenon:

An LFO leakage like clock occurs when the following controls are set at the following points:

> : "3" "RATE" in LFO-1 section "WAVEFORM" in LFO-1 section : 11 "ENV-2 LEVEL" in VCA section : "10" "LFO" in VCA section : "10"

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The cause and countermeasure :

The cause is that LFO output always jump into VCA circuit directly because LFO circuit is very near to VCA circuit on the printed circuit board. The countermeasure is to make a reasonable distance between VCA and LFO circuits on the board, and detail of solution has been shown in JP-6 service manual (at page 11).

NOTE: This modification has already been carried out on JP-6 from the serial number 301700, and this countermeasure is effective on JP-6 with serial numbers from 250100 to 301699.

